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Tamara Alcaraz

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Yoshinao MORIKAWA

Serial No.: To Be Assigned

Filing Date: Herewith

For: REFERENCE VOLTAGE
GENERATION CIRCUIT FOR
SEMICONDUCTOR MEMORY
DEVICE, MEMORY READING
CIRCUIT INCLUDING THE SAME,
AND ELECTRONIC INFORMATION
DEVICE INCLUDING THE SAME

Examiner: To Be Assigned

Group Art Unit: To Be Assigned

PRELIMINARY AMENDMENT

Box Patent Application
U.S. Patent and Trademark Office
Washington D.C. 20231

Dear Sir:

Prior to examination on the merits, applicants respectfully request that the following amendment be entered.

AMENDMENTS

In the Specification:

On page 1, under BACKGROUND OF THE INVENTION, please insert the following paragraph:

--CROSS-REFERENCE TO RELATED APPLICATIONS

This application claims priority to Japanese Patent Application Number 2001-057499 filed March 1, 2001, the content of which is incorporated herein by reference in its entirety. --

REMARKS

Entry of the foregoing amendment is respectfully requested. The specification has been amended to provide cross-reference to a related patent application.

In the unlikely event that the transmittal letter is separated from this document and the Patent Office determines that an extension and/or other relief is required, applicant petitions for any required relief including extensions of time and authorizes the Assistant Commissioner to charge the cost of such petitions and/or other fees due in connection with the filing of this document to Deposit Account No. 03-1952 referencing docket no. 299002054200. However, the Assistant Commissioner is not authorized to charge the cost of the issue fee to the Deposit Account.

Respectfully submitted,

Dated: February 8, 2002

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